Features

- Formerly a KEKO VARICON product
- 12 V, 24 V and 42 V supply voltages
- Broad range of current and energy handling capability
- In-line leads
- Available in tape and reel for automatic insertion equipment
- +125 °C maximum continuous operating temperature
- AEC-Q200 compliancy available upon request

AV Series – Automotive Grade Through-hole Varistors

General Information

Almost all electronic systems in an automobile, e.g. anti-lock brake system, direct ignition system, airbag control system, wiper motors, etc., are susceptible to damage from destructive voltage transients.

The AV series of leaded varistors includes multilayer through-hole varistors. Automotive grade varistors are intended for WLD applications typically requiring up to 50 J of energy, and disc automotive grade varistors for WLD applications requiring more than 50 J of energy.

Through-hole varistors offer excellent transient energy absorption due to improved internal energy distribution. Compared to equivalent disc varistors, they offer better electrical characteristics in a much smaller size.

Absolute Maximum Ratings

Parameter	Value	Units
Continuous:		
Steady State Applied Voltage		
DC Voltage Range (V_{dc})	18 to 56	V
Transient:		
Load Dump Energy, (WLD)	3 to 25***	J
Jump Start Capability (5 minutes), (V_{jump})	24.5 to 65	V
Peak Single Pulse Surge Current, 8/20 μs Waveform (I_{max})	400 to 2000	A
Single Pulse Surge Energy, 10/1000 μs Waveform (W_{max})	1.6 to 76	J
Operating Ambient Temperature	-55 to +125	°C
Storage Temperature Range	-55 to +150	°C
Threshold Voltage Temperature Coefficient	< +0.05	%/°C
Insulation Resistance	>1	GΩ
Isolation Voltage Capability	>1.25	kV
Response Time	< 25	ns
Climatic Category	55 / 125 / 56	

*** Varistors with WLD = 50 J and 100 J in the form of leaded multilayer or single layer disc varistors are available upon request.

Additional Information

Click these links for more information:

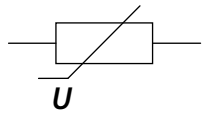
PRODUCT SELECTOR | TECHNICAL LIBRARY | INVENTORY | SAMPLES | CONTACT

Agency Recognition

Standard	UL 1449
File Number	E313168**

**Not all rated voltages are UL recognized; check the file for details.

Varistor Symbol



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WARNING Cancer and Reproductive Harm
www.P65Warnings.ca.gov

AV Series – Automotive Grade Through-hole Varistors BOURNS®

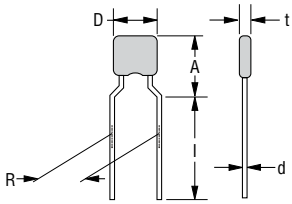
Device Ratings

Model	V_{rms}	V_{dc}	V_n @ 1 mA	V_{jump} 5 min.	V_c	I_c	I_{max} 8/20 μs	W_{max} 10/1000 μs	WLD 10x	P max.	C typ. @ 1 kHz	t max.
	V	V	V	V	V	A	A	J	J	W	nF	mm
12 V Power Supply												
AV 14 K 602 003	14	16	24	24.5	40	2.5	400	1.6	3	0.010	2.5	4.5
AV 14 K 802 006	14	16	24	24.5	40	5	800	2.4	6	0.015	4.6	4.5
AV 14 K 902 012	14	16	24	24.5	40	5	1200	4.4	12	0.030	10.5	4.5
AV 14 K 902 025	14	16	24	24.5	40	10	2000	6.0	25	0.080	22.0	5.5
AV 14 K 1103 050	14	16	24	24.5	40	10	2000	13.2	50	0.100	29.0	6.5
AV 17 K 602 003	17	20	27	30	44	2.5	400	1.8	3	0.010	2.0	4.5
AV 17 K 802 006	17	20	27	30	44	5	800	2.9	6	0.015	4.0	4.5
AV 17 K 902 025	17	20	27	30	44	10	2000	7.2	25	0.080	18.0	5.5
AV 17 K 1103 050	17	20	27	30	44	10	2000	15.8	50	0.100	24.0	6.5
24 V Power Supply												
AV 20 K 602 003	20	26	33	30	54	2.5	400	1.9	3	0.010	1.8	4.5
AV 20 K 802 006	20	26	33	30	54	5	800	3.0	6	0.015	3.5	4.5
AV 20 K 902 025	20	26	33	30	54	10	2000	9.0	25	0.080	13.0	4.5
AV 20 K 1103 050	20	26	33	30	54	10	2000	17.0	50	0.100	18.0	6.5
AV 30 K 602 003	30	34	47	50	77	2.5	400	2.3	3	0.010	1.3	4.5
AV 30 K 802 006	30	34	47	50	77	5	800	3.8	6	0.015	2.0	4.5
AV 30 K 902 025	30	34	47	50	77	10	2000	18.0	25	0.080	12.0	4.5
42 V Power Supply												
AV 40 K 602 003	40	56	68	65	110	2.5	400	2.6	3	0.010	1.1	4.5
AV 40 K 802 006	40	56	68	65	110	5	800	4.8	6	0.015	1.8	4.5
AV 40 K 902 025	40	56	68	65	110	10	2000	18.0	25	0.080	6.6	4.5

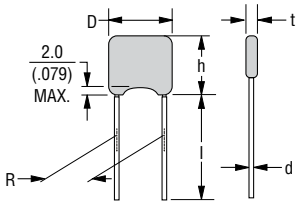
35 V Power Supply available upon request.

Product Dimensions

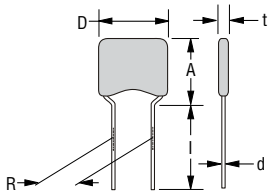
Size 602
WLD Code 003



Size 802
WLD Code 006



Size 902, 1103
WLD Code 012, 025, 050



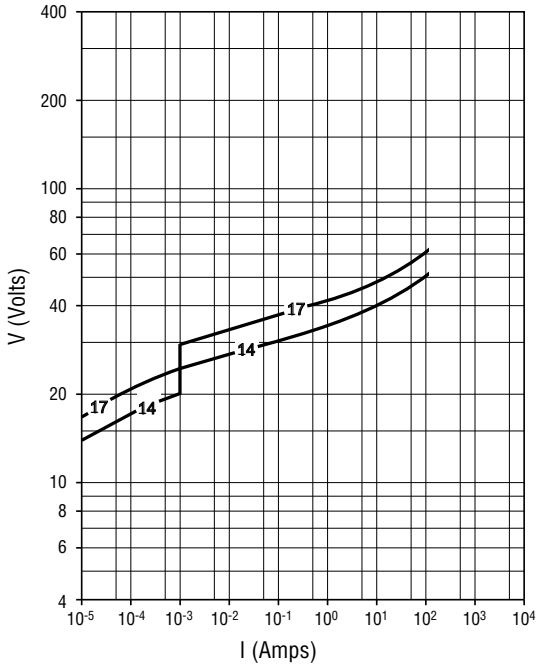
Size	D max.	R	d	h/A max.
602	7.0 (.276)	5.0 (.197)	0.6 (.024)	7.0 (.276)
802	8.0 (.315)	5.0 (.197)	0.6 (.024)	9.0 (.354)
902	9.0 (.354)	5.0 (.197)	0.6 (.024)	12.0 (.472)
1103	11.0 (.433)	7.5 (.295)	0.6 (.024)	12.0 (.472)

DIMENSIONS: MM / (INCHES)

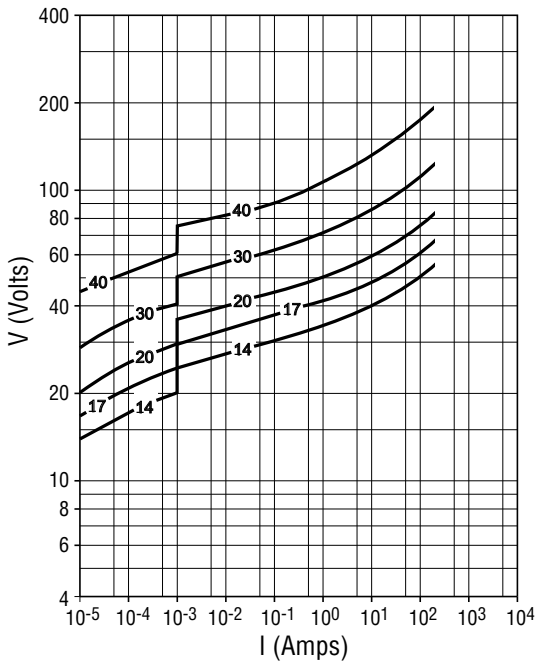
Dimension “t” appears in the Device Ratings table above.

Protection Level

Model Size 602 (AV 14 K 602 003 ~ AV 40 K 602 003)

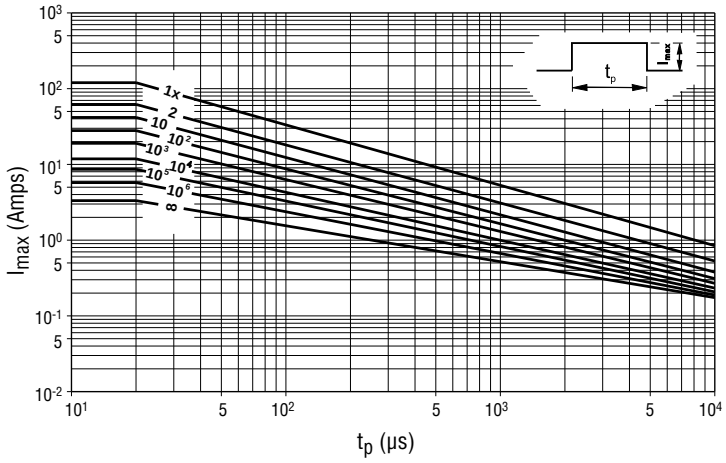


Model Size 802 (AV 14 K 802 006 ~ AV 40 K 802 006)

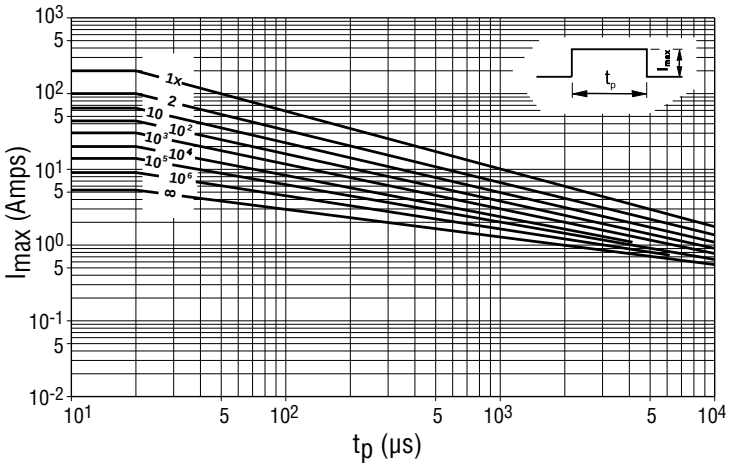


Pulse Rating Curves

Model Size 602 (AV 14 K 602 003 ~ AV 40 K 602 003)

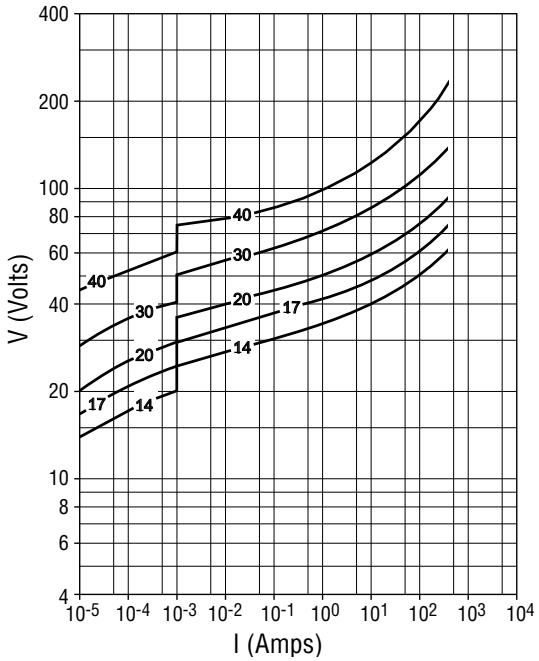


Model Size 802 (AV 14 K 802 006 ~ AV 40 K 802 006)

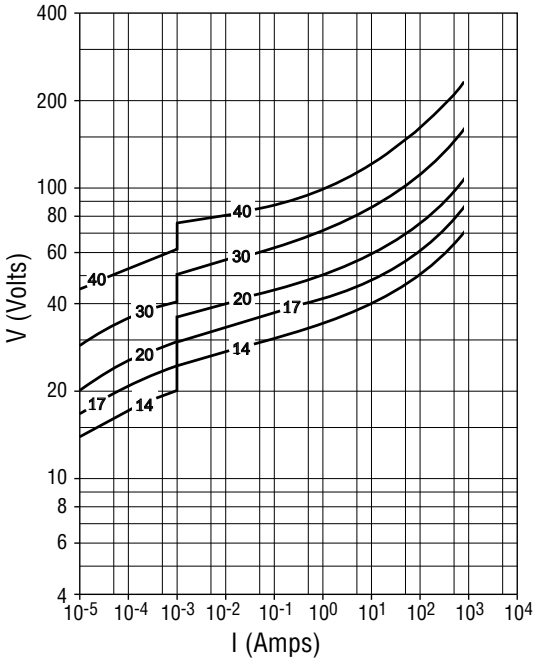


Protection Level

Model Size 902 (AV 14 K 902 012)

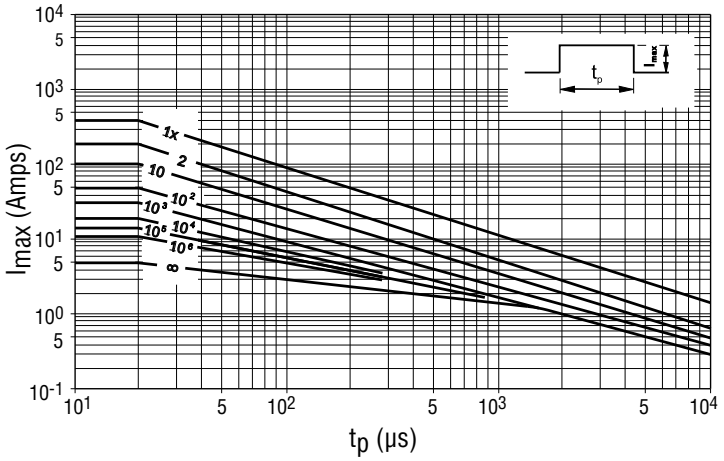


Model Size 902, 1103
(AV 14 K 902 025 ~ AV40 K 902 025
& AV 14 K 1103 050 ~ AV 40 K 1103 050)

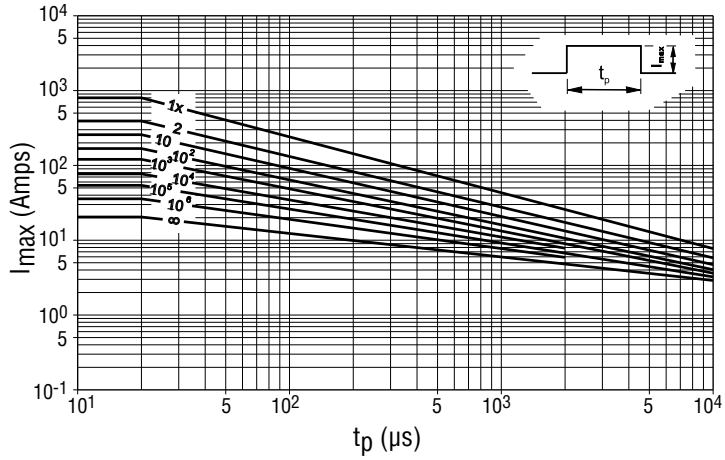


Pulse Rating Curves

Model Size 902 (AV 14 K 902 012)



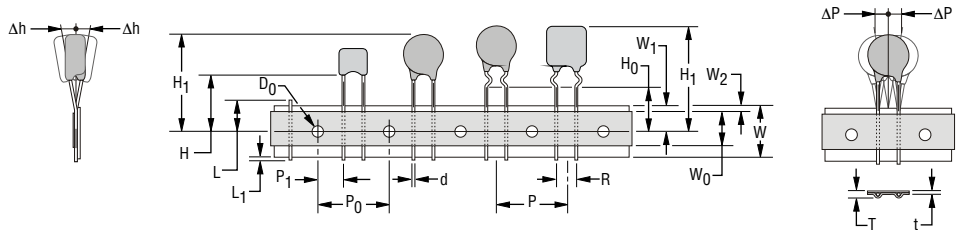
Model Size 902, 1103
(AV 14 K 902 025 ~ AV40 K 902 025
& AV 14 K 1103 050 ~ AV 40 K 1103 050)



Packaging Specifications - Tape

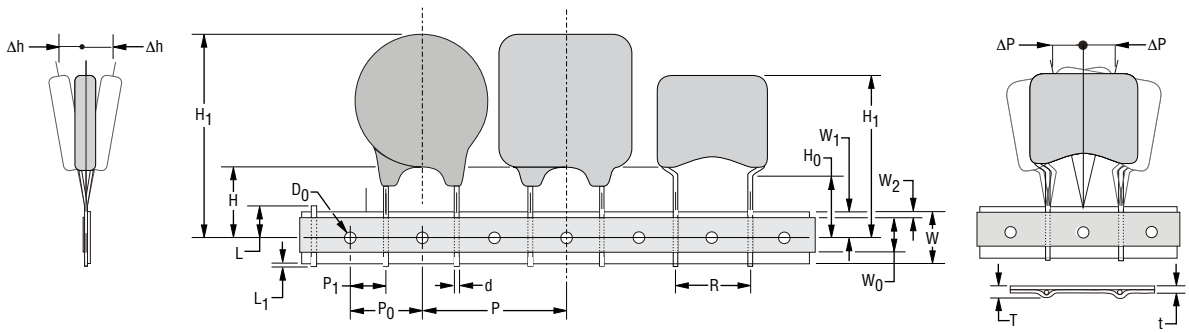
Conforms to IES Publication 286-2 Ed. 3: 2008-03

Dimension R = 5 mm



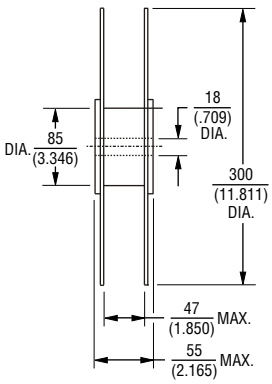
Dimensions on Next Page

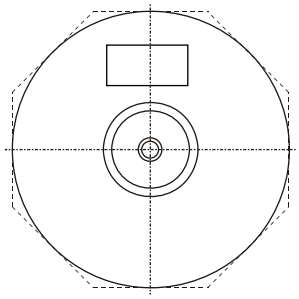
Dimension R = 7.5 mm & 10 mm



Dimensions on Next Page

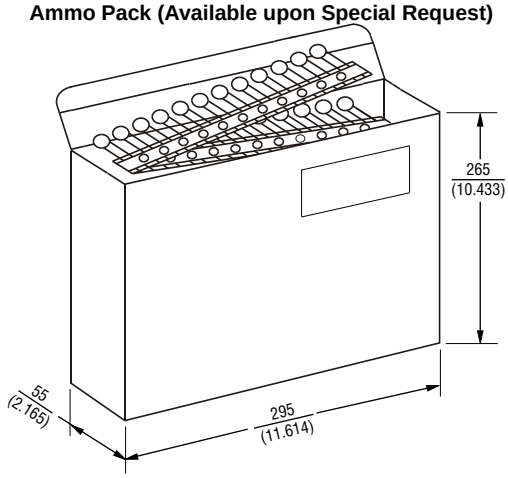
Reel





DIMENSIONS: MM (INCHES)

Ammo Pack (Available upon Special Request)



Packaging Specifications - Tape (Continued)

Symbol	Parameter	Model Size	
		602/802/902	1103
W	Carrier tape width	18 +1.0/-0.5 (.709 +.039/-.020)	
W_0	Hold down tape width	5 (.197) MIN.	
W_1	Sprocket hole position	9 +0.75/-0.5 (.354 +.030/-.020)	
W_2	Distance between the upper edges of the carrier tape and hold down tape	3 (.118) MAX.	
T	Total tape thickness	1.5 (.059) MAX.	1.7 (.067) MAX.
t	Tape thickness	0.9 (.035) MAX.	
P	Pitch of component	12.7 ± 1.0 (.500 ± .039)	25.4 ± 1.0 (1.000 ± .039)
P_0	Feed hole pitch	12.7 ± 0.3 (.500 ± .012)	
P_1	Feed hole center to pitch	3.85 ± 0.7 (.152 ± .028)	8.95± 0.7 (.352 ± .028)
R	Lead spacing	5 +0.5/-0.2 (.197 +.020/-.008)	7.5 +0.5/-0.2 (.295 +.020/-.008)
ΔP	Component alignment	±1.3 (±.051) MAX.	
Δh	Component alignment	±2 (±.079) MAX.	
d	Wire diameter	0.6 (.024) MAX.	0.8 (.031) MAX.
D_0	Feed hold diameter	4 ± 0.2 (.157 ± .008)	
H	Height from tape center to component base	18 +2.0/-0.0 (.709 +.079/-.000)	
H_0	Seating plane height	16 ± 0.5 (.630 ± .020)	
H_1	Component height	32.2 (1.268) MAX.	46.5 (1.831) MAX.
L	Protrusion - cut out	11 (.433) MAX.	
L_1	Protrusion - cut off	0.5 (.020) MAX.	

DIMENSIONS: MM (INCHES)

Packaging Quantities

Bulk

V	602	802	902	1103
14	1500	1500	1000	800
17	1500	1500	1000	800
20	1500	1500	1000	800
30	1500	1500	1000	
40	1500	1500	1000	

Reel

V	602	802	902	1103
14	1500	1500	1500	1300
17	1500	1500	1300	1300
20	1500	1500	1300	1300
30	1300	1300	1300	
40	1300	1300	1300	

How to Order

AV14K602003RL1yy

Series Designator
- AV = AV Series

Max. Continuous Working Voltage (V_{rms})

V_n Tolerance
- K = ± 10 %

Model Size
- • 602 • 802 • 902 • 1103

WLD Load Dump Energy Code
- • 003 = 3 J • 012 = 12 J • 050 = 50 J
- • 006 = 6 J • 025 = 25 J

Packaging
- R = Reel
- B = Bulk
- A = Ammo Pack (Available only upon request)

Lead Style
- 1 = Straight
- 5 = Crimped

Special Requirements
- • yy

Instructions for Creating Orderable Part Number:

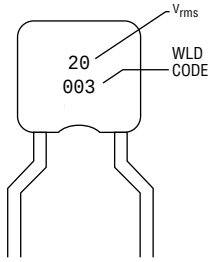
1) Start with base part number in characteristics table (example: AV14K602003).

2) Add Packaging: R (example part number becomes AV14K602003R).

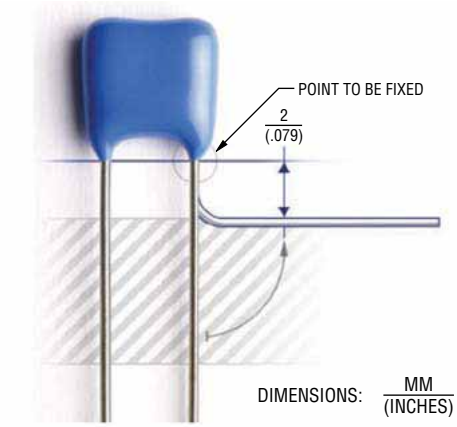
3) Add Lead Style: L1 (example part number becomes AV14K602003RL1).

4) Part number can have no spaces or lower case letters.

Typical Part Marking



Assembly Recommendations for Through-Hole Components



Very often before soldering through-hole components, their leads get bent. It is important not to damage the components during lead bending. Typical damage incurred during bending is cracks in epoxy parts, which can lead to increased humidity sensitivity of a component and, consequentially, to a shorter lifetime.

In order to avoid epoxy damage, it is necessary to:
- fix the most sensitive point (epoxy parts) of a component body
- bend the wire at least 2 mm below the end of epoxy parts

Other potential damage to a component which can lead to component failure or a shorter lifetime is thermal shock during manual soldering with a soldering iron. This can occur when a soldering iron is placed too close to one point of the component body and most often it happens if the solder joint is too close to the varistor body.

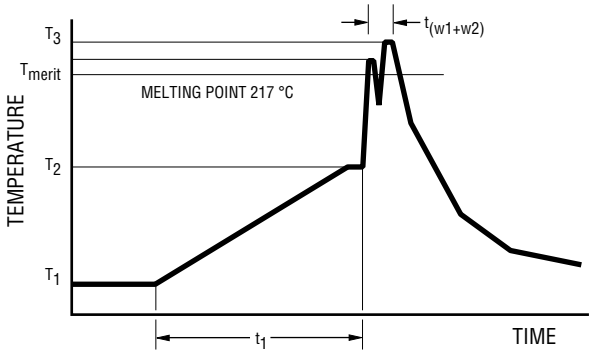
Resistance to Soldering Heat

In the case of automatic wave soldering, it is important to provide sufficient resistance to soldering heat. In order to prevent any potential problems, internal standards were introduced for testing the resistance to soldering heat of through-hole components: 300 °C, 10 seconds.

Pb-free Wave Soldering Profile Recommendations

Recommended soldering profiles for all above components are in accordance with JEDEC standard curves (J-STD-020D) and are, therefore, compatible with the Pb-free process.

Lead-free Wave Soldering Profile - Pb-free wave profile requirements for soldering heat resistance of components



Parameter	Symbol	Specification
Preheating temperature gradient		4 °C/sec. max.
Preheating time	t_1	2 to 5 min.
Min. preheating temperature	T_1	130 °C
Max. preheating temperature	T_2	180 °C
Melting temperature/point	T_{meltv}	217 °C
Time in wave soldering phase (w_1+w_2)	t_{w1+w2}	10 sec.
Max. wave temperature (w_1+w_2)	T_s	265 °C +0/-5 °C
Cooling temperature gradient		6° C/sec. max.
Temperature jump from T_2 to T_3 (w_1)	$T_{3(w1)}$ - T_2	120 °C max
Time from 25 °C to T_3 (wave temperature)		8 min. max.

Reliability Testing Procedures

Varistor test procedures comply with CECC 42200, IEC 1051-1/2 (and AEC-Q200, if applicable). Test results are available upon customer request. Special tests can be performed upon customer request.

Reliability Parameter	Test	Tested According to	Condition to be Satisfied after Testing
AC/DC Bias Reliability	AC/DC Life Test	CECC 42200, Test 4.20 or IEC 1051-1, Test 4.20, AEC-Q200 Test 8 - 1000 h at UCT	$\lvert\delta V_n$ (1 mA)$\rvert < 10$ %
Pulse Current Capability	I_{max} 8/20 μs	CECC 42200, Test C 2.1 or IEC 1051-1, Test 4.5 10 pulses in the same direction at 2 pulses per minute at maximum peak current for 10 pulses	$\lvert\delta V_n$ (1 mA)$\rvert < 10$ % no visible damage
Pulse Energy Capability	W_{max} 10/1000 μs	CECC 42200, Test C 2.1 or IEC 1051-1, Test 4.5 10 pulses in the same direction at 1 pulse every 2 minutes at maximum peak current for 10 pulses	$\lvert\delta V_n$ (1 mA)$\rvert < 10$ % no visible damage
WLD Capability	WLD x 10	ISO 7637, Test pulse 5, 10 pulses at rate of 1 per minute	$\lvert\delta V_n$ (1 mA)$\rvert < 15$ % no visible damage
V_{jump} Capability	V_{jump} 5 min.	Increase of supply voltage to $V \geq V_{jump}$ for 1 minute	$\lvert\delta V_n$ (1 mA)$\rvert < 15$ % no visible damage
Environmental and Storage Reliability	Climatic Sequence	CECC 42200, Test 4.16 or IEC 1051-1, Test 4.17 a) Dry heat, 16h, UCT, Test Ba, IEC 68-2-2 b) Damp heat, cyclic, the first cycle: 55 °C, 93 % RH, 24 h, Test Db 68-2-4 c) Cold, LCT, 2 h, Test Aa, IEC 68-2-1 d) Damp heat cyclic, remaining 5 cycles: 55 °C, 93 % RH, 24 h/cycle, Test Bd, IEC 68-2-30	$\lvert\delta V_n$ (1 mA)$\rvert < 10$ %
	Thermal Shock	CECC 42200, Test 4.12, Test Na, IEC 68-2-14, AEC-Q200 Test 16, 5	$\lvert\delta V_n$ (1 mA)$\rvert < 10$ % no visible damage
	Steady State Damp Heat	CECC 42200, Test 4.17, Test Ca, IEC 68-2-3, AEC-Q200 Test 6, 56 days, 40 °C, 93 % RH, AEC-Q200 Test 7: Bias, Rh, T all at 85.	$\lvert\delta V_n$ (1 mA)$\rvert < 10$ %
	Storage Test	IEC 68-2-2, Test Ba, AEC-Q200 Test 3, 1000 h at maximum storage temperature	$\lvert\delta V_n$ (1 mA)$\rvert < 5$ %

Continued on Next Page

Reliability Testing Procedures (Continued)

Reliability Parameter	Test	Tested According to	Condition to be Satisfied after Testing
Mechanical Reliability	Solderability	CECC 42200, Test 4.10.1, Test Ta, IEC 68-2-20 solder bath and reflow method	Solderable at shipment and after 2 years of storage, criteria: >95% must be covered by solder for reflow meniscus
	Resistance to Soldering Heat	CECC 42200, Test 4.10.2, Test Tb, IEC 68-2-20 solder bath nad reflow method	$\lvert\delta V_n$ (1 mA)$\rvert < 5$ %
	Terminal Strength	JIS-C-6429, App. 1, 18N for 60 sec. - same for AEC-Q200 Test 22	No visual damage
	Board Flex	JIS-C-6429, App. 2, 2 mm min. AEC-Q200 test 21 - Board flex: 2 mm flex min.	$\lvert\delta V_n$ (1 mA)$\rvert < 2$ % No visible damage
	Vibration	CECC 42200, Test 4.15, Test Fc, IEC 68-2-6, AEC-Q200 Test 14 Frequency range 10 to 55 Hz (AEC: 10-2000 Hz) Amplitude 0.75 m/s^2 or 98 m/s^2 (AEC: 5 g for 20 minutes) Total duration 6 h (3x2 h) (AEC: 12 cycles each of 3 directions) Waveshape - half sine	$\lvert\delta V_n$ (1 mA)$\rvert < 2$ % No visible damage
	Mechanical Shock	CECC 42200, Test 4.14, Test Ea, IEC 68-2-27, AEC-Q200 Test 13. Acceleration = 490 m/s^2 (AEC: MIL-STD-202-Method 213), Pulse duration = 11 ms, Waveshape - half sine; Number of shocks = 3x6	$\lvert\delta V_n$ (1 mA)$\rvert < 10$ % No visible damage
Electrical Transient Conduction	ISO-7637-1 Pulses	AEC-Q200 Test 30: Test pulses 1 to 3. Also other pulses - freestyle.	$\lvert\delta V_n$ (1 mA)$\rvert < 10$ % No visible damage

Terminology

Term	Symbol	Definition
Rated AC Voltage	V_{rms}	Maximum continuous sinusoidal AC voltage (<5 % total harmonic distortion) which may be applied to the component under continuous operation conditions at +25 °C
Rated DC Voltage	V_{dc}	Maximum continuous DC voltage (<5 % ripple) which may be applied to the component under continuous operating conditions at +25 °C
Supply Voltage	V	The voltage by which the system is designated and to which certain operating characteristics of the system are referred; V_{rms} = 1.1 x V
Leakage Current	I_{dc}	The current passing through the varistor at V_{dc} and at +25 ° or at any other specified temperature
Varistor Voltage	V_n	Voltage across the varistor measured at a given reference current (I_n)
Reference Current	I_n	Reference current = 1 mA DC
Clamping Voltage Protection Level	V_c	The peak voltage developed across the varistor under standard atmospheric conditions, when passing an 8/20 μs class current pulse
Class Current	I_c	A peak value of current which is 1/10 of the maximum peak current for 100 pulses at two per minute for the 8/20 μs pulse
Voltage Clamping Ratio	V_c/V_{app}	A figure of merit measure of the varistor clamping effectiveness as defined by the symbols V_c/V_{app}, where (V_{app} = V_{rms} or V_{dc})
Jump Start Transient	V_{jump}	The jump start transient results from the temporary application of an overvoltage in excess of the rated battery voltage. The circuit power supply may be subjected to a temporary overvoltage condition due to the voltage regulation failing or it may be deliberately generated when it becomes necessary to boost start the car.
Rated Single Pulse Transient Energy	W_{max}	Energy which may be dissipated for a single 10/1000 μs pulse of a maximum rated current, with rated AC voltage or rated DC voltage also applied, without causing device failure
Load Dump Transient	WLD	Load Dump is a transient which occurs in automotive environments. It is an exponentially decaying positive voltage which occurs in the event of a battery disconnect while the alternator is still generating charging current with other loads remaining on the alternator circuit at the time of battery disconnect.
Rated Peak Single Pulse Transient Current	I_{max}	Maximum peak current which may be applied for a single 8/20 μs pulse, with rated line voltage also applied, without causing device failure
Rated Transient Average Power Dissipation	P	Maximum average power which may be dissipated due to a group of pulses occurring within a specified isolated time period, without causing device failure at 25 °C
Capacitance	C	Capacitance between two terminals of the varistor measured @ 1 kHz
Non-linearity Exponent	α	A measure of varistor nonlinearity between two given operating currents, I_n and I_1 as described by I = k V exp(a), where: - k is a device constant, - $I_1 < I < I_n$ and - a log $(I_1/I_n)/\log(V_1/V_n) = 1/\log (V_1/V_n)$, where: - I_r is reference current (1 mA) and V_n is varistor voltage - I_1 = 10 I_n, V_1 is the voltage measured at I_1
Response Time	tr	The time lag between application of a surge and varistor's "turn-on" conduction action
Varistor Voltage Temperature Coefficient	TC	(V_n @ 85 °C - V_n @ 25 °C) / (V_n @ 25 °C) x 60 °C) x 100
Insulation Resistance	IR	Minimum resistance between shorted terminals and varistor surface
Isolation Voltage		The maximum peak voltage which may be applied under continuous operating conditions between the varistor terminations and any conducting mounting surface
Operating Temperature		The range of ambient temperature for which the varistor is designed to operate continuously as defined by the temperature limits of its climatic category
Climatic Category	LCT/UCT/DHD	LCT & UCT = Lower and Upper Category Temperature - the minimum and maximum ambient temperatures for which a varistor has been designed to operate continuously. DHD = Dump Heat Test Duration
Storage Temperature		Storage temperature range without voltage applied
Current/Energy Derating		Derating of maximum values when operated above UCT

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